

International IOR Rectifier

111CNQ045A

SCHOTTKY RECTIFIER
New GenIII D-61 Package

110 Amp

Major Ratings and Characteristics

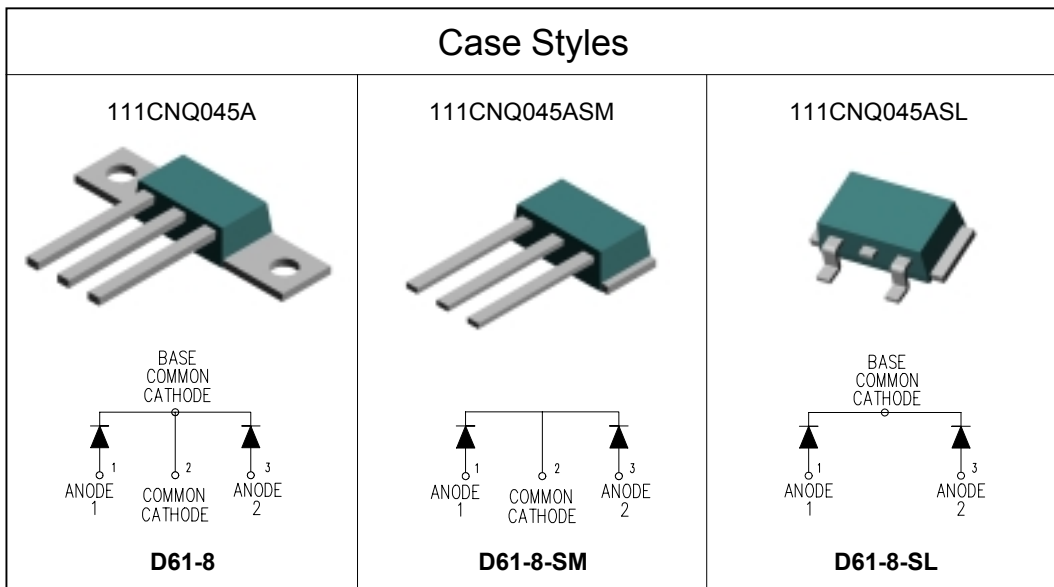
| Characteristics | 111CNQ045A | Units |
|---|------------|------------------|
| $I_{F(AV)}$ Rectangular waveform | 110 | A |
| V_{RRM} | 45 | V |
| I_{FSM} @tp=5 μ s sine | 4000 | A |
| V_F @55Apk, $T_J=125^\circ\text{C}$ (per leg) | 0.55 | V |
| T_J range | -55 to 175 | $^\circ\text{C}$ |

Description/Features

The 111CNQ045A center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 175 $^\circ\text{C}$ junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- 175 $^\circ\text{C}$ T_J operation
- Center tap module
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Low profile, small footprint, high current package
- *New fully transfer-mold low profile, small footprint, high current package*

Case Styles



Voltage Ratings

| | |
|--|------------|
| Part number | 111CNQ045A |
| V _R Max. DC Reverse Voltage (V) | 45 |
| V _{RWM} Max. Working Peak Reverse Voltage (V) | |

Absolute Maximum Ratings

| Parameters | 111CNQ | Units | Conditions |
|--|-------------|-------|---|
| I _{F(AV)} Max. Average Forward Current * See Fig. 5 | 55 110 | A | 50% duty cycle @ T _C = 152 °C, rectangular wave form |
| I _{FSM} Max. Peak One Cycle Non-Repetitive Surge Current (Per Leg) * See Fig. 7 | 4000 600 | A | 5µs Sine or 3µs Rect. pulse 10ms Sine or 6ms Rect. pulse |
| E _{AS} Non-Repetitive Avalanche Energy (Per Leg) | 54 | mJ | T _J = 25 °C, I _{AS} = 8 Amps, L = 1.7 mH |
| I _{AR} Repetitive Avalanche Current (Per Leg) | 8 | A | Current decaying linearly to zero in 1 µsec Frequency limited by T _J max. V _A = 1.5 x V _R typical |

Electrical Specifications

| Parameters | 111CNQ | Units | Conditions |
|--|------------------------------|-------|---|
| V _{FM} Max. Forward Voltage Drop (Per Leg) (1) | 0.61 0.75 0.55 0.69 | V | @ 55A @ 110A @ 55A @ 110A |
| I _{RM} Max. Reverse Leakage Current (Per Leg) (1) | 1.5 65 | mA | T _J = 25 °C T _J = 125 °C |
| C _T Max. Junction Capacitance (Per Leg) | 3900 | pF | V _R = 5V _{DC} , (test signal range 100Khz to 1Mhz) 25°C |
| L _S Typical Series Inductance (Per Leg) | 5.5 | nH | Measured lead to lead 5mm from package body |
| dv/dt Max. Voltage Rate of Change (Rated V _R) | 10000 | V/µs | |

(1) Pulse Width < 300µs, Duty Cycle <2%

Thermal-Mechanical Specifications

| Parameters | 111CNQ | Units | Conditions |
|---|----------------------------|-------------------|--|
| T _J Max. Junction Temperature Range | -55 to 175 | °C | |
| T _{stg} Max. Storage Temperature Range | -55 to 175 | °C | |
| R _{thJC} Max. Thermal Resistance Junction to Case (Per Leg) | 0.5 | °C/W | DC operation |
| R _{thJC} Max. Thermal Resistance Junction to Case (Per Package) | 0.25 | °C/W | DC operation |
| R _{thCS} Typical Thermal Resistance, Case to Heatsink (D61-8 Only) | 0.30 | °C/W | Mounting surface, smooth and greased Device flatness < 5 mils |
| wt Approximate Weight | 7.8(0.28) | g(oz.) | |
| T Mounting Torque (D61-8 Only) | Min. 40(35) Max. 58(50) | Kg-cm (lbf-in) | |

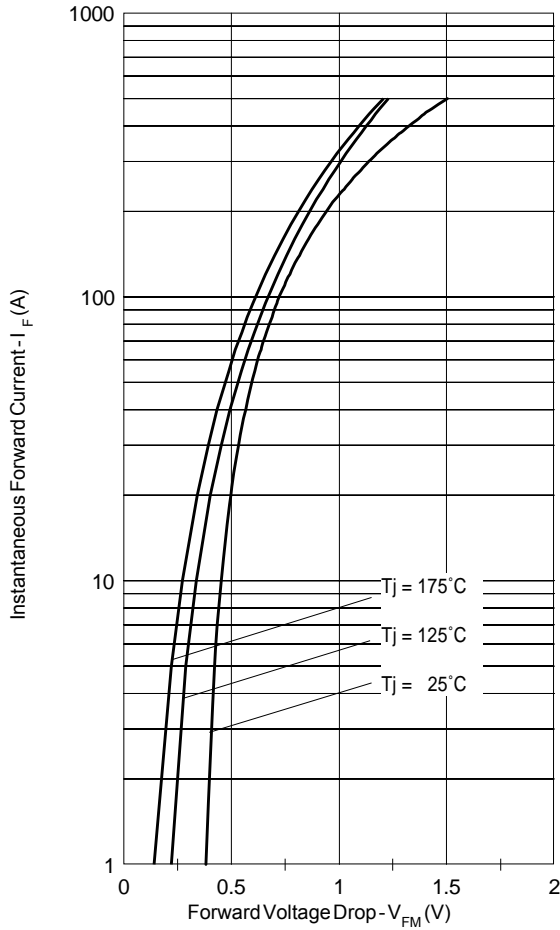


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

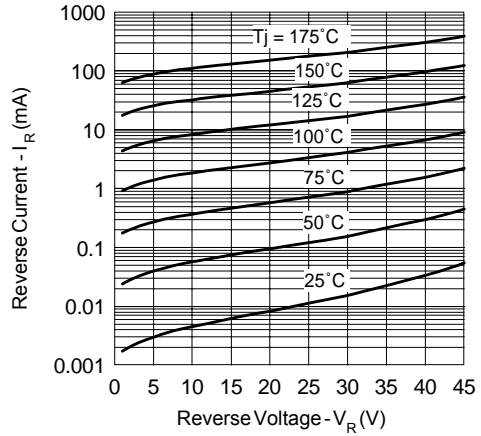


Fig. 2 - Typical Values Of Reverse Current Vs. Reverse Voltage (Per Leg)

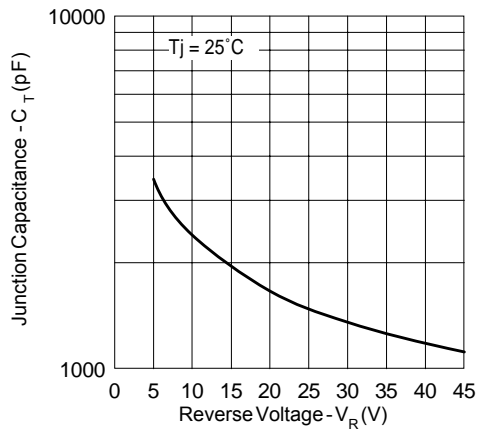


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

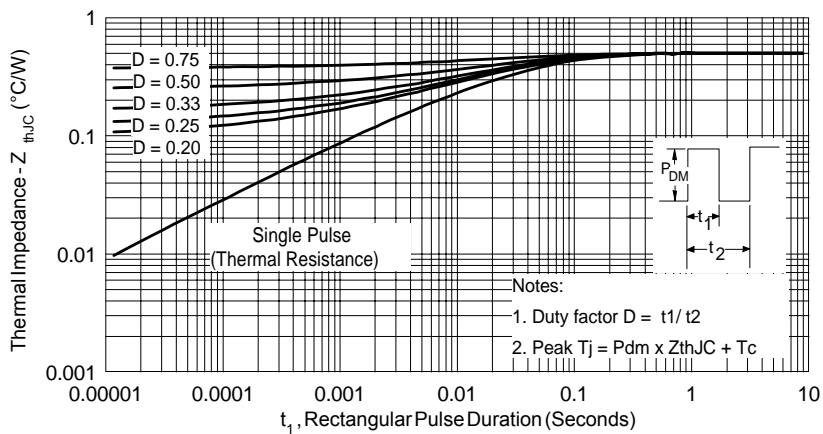


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

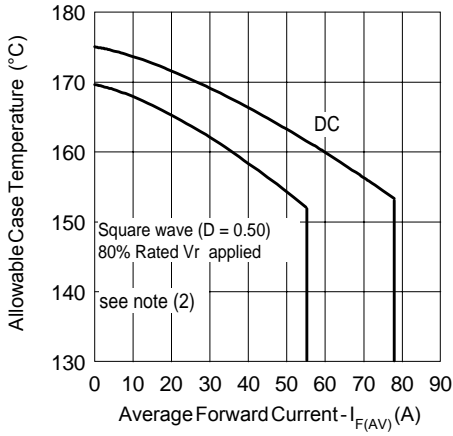


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

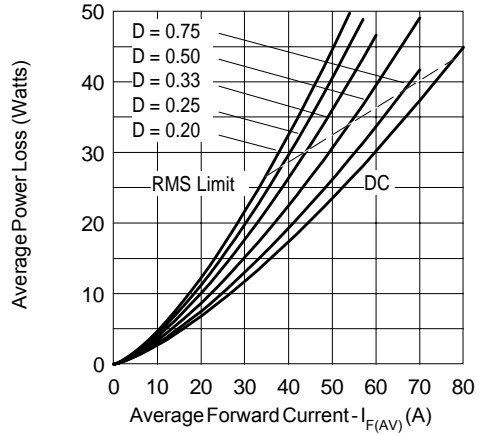


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

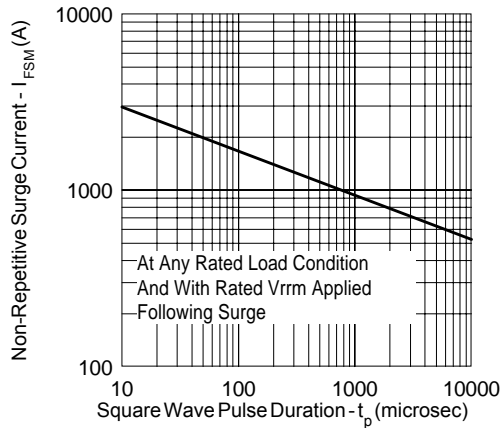


Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

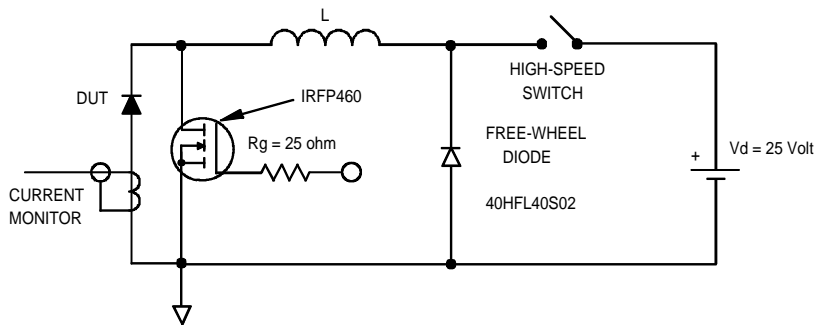
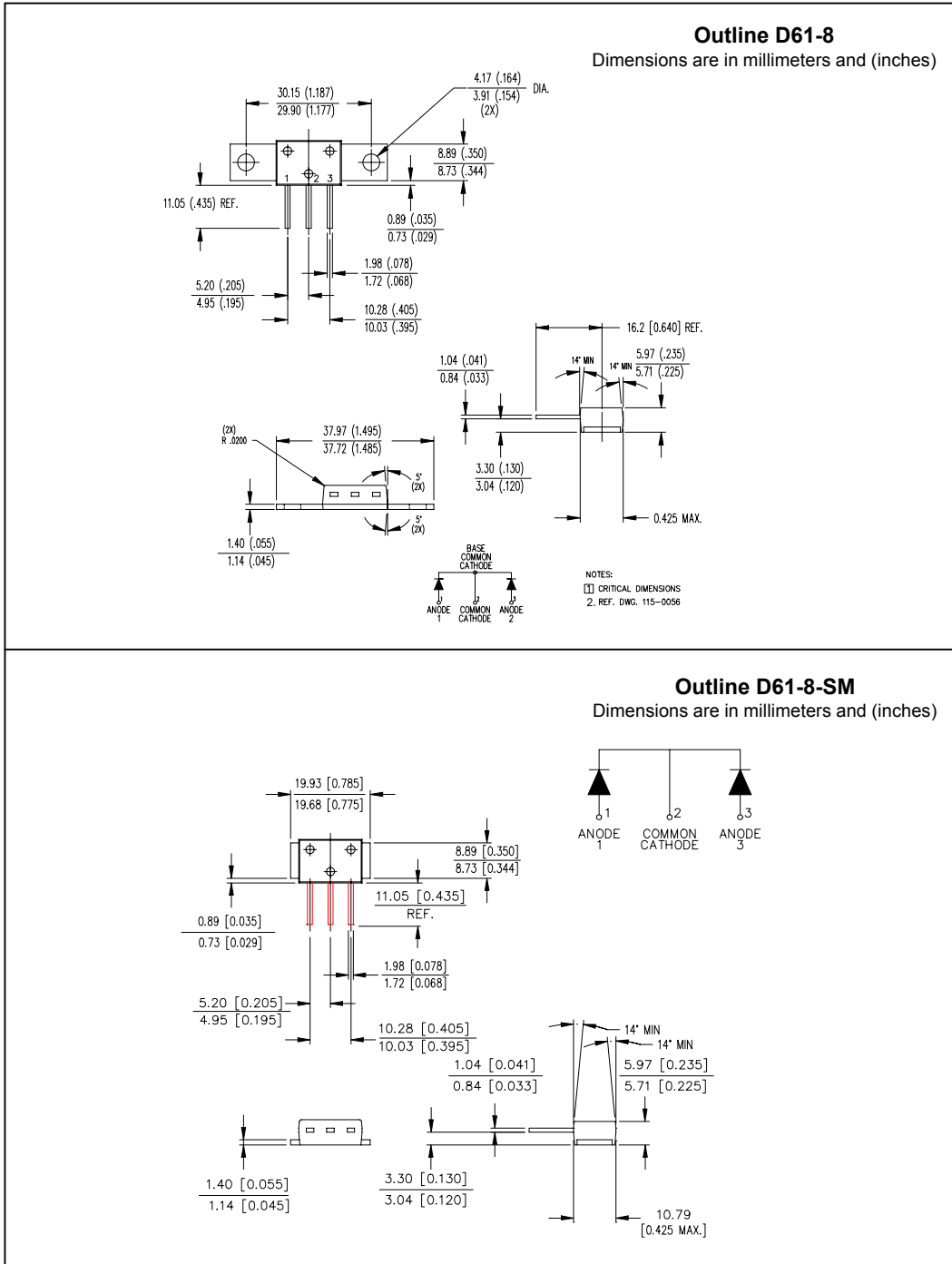


Fig. 8 - Unclamped Inductive Test Circuit

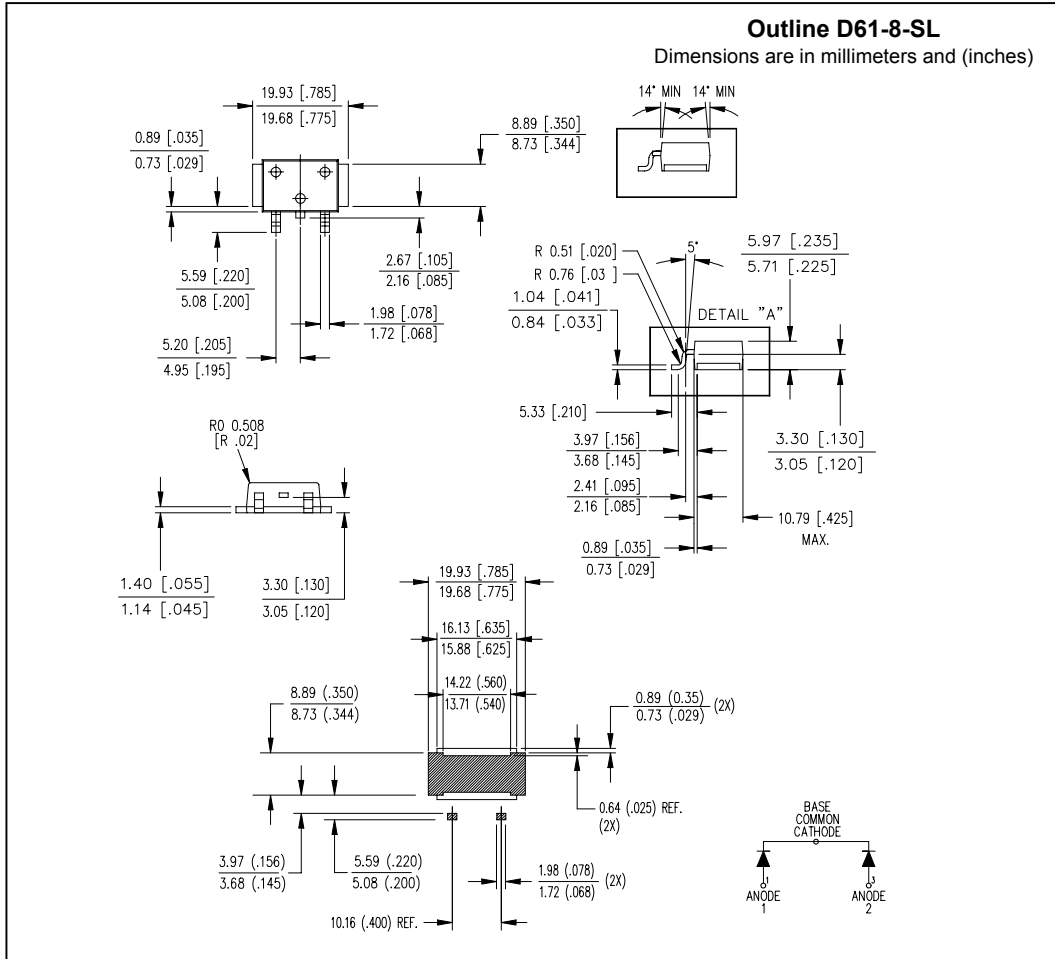
- (2) Formula used: $T_c = T_j - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 $P_d = \text{Forward Power Loss} = I_{F(AV)} \times V_{FM} @ (I_{F(AV)} / D)$ (see Fig. 6);
 $P_{d_{REV}} = \text{Inverse Power Loss} = V_{R1} \times I_R (1 - D)$; $I_R @ V_{R1} = 80\% \text{ rated } V_R$

Outline Table

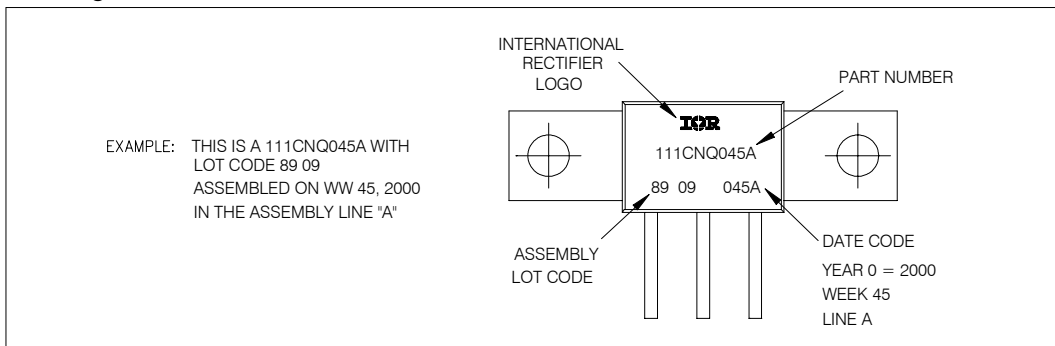


Outline D61-8-SM
 Dimensions are in millimeters and (inches)

Outline Table



Marking Information



Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

International
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